LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (Autonomous)
L.B.REDDY NAGAR, MYLAVARAM-521 230.A.P. INDIA
Affiliated to JNTUK, Kakinada & Approved by AICTE, New Delhi
NAAC Accredited with "A" grade, Accredited by NBA
New Delhi & Certified by ISO 9001:2015
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
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Minutes of EEE Department BOS meeting held on 4th March, 2017

The Board of Studies meeting held on 04-03-2017 in the department of EEE discussed the following items of R-17 regulations of B.Tech programme.

A) Detailed discussion on the following has been done:
   i) Course structure
   ii) Electives to be offered under PE & OE
   iii) Add-on courses
   iv) Audit & Mandatory courses

B) The following items were also discussed and approved.
   i) Course structure for 4 - Year B.Tech course of R-17 regulations.
   ii) Detailed syllabus of B.Tech I semester to IV semester along with outcomes of each course.
   iii) The panelsof external examiners for all department core activities, during the four A.Y.s i.e. 2017-18 to 2020-21.
   - Paper setters for all department theory courses
   - Paper valuators for spot and external evaluation (department core)
   - Project work adjudication
   - All department laboratory courses

**Details of discussions and deliberation are enclosed as Annexure

Name & signature of BoS Chairman: Dr.M.UmaVani 4/3/2017

Names & signatures of External members:

1. Dr. P.V.Ramana Rao -
2. Dr. M.B.Srinivas -
3. Er. Ch. Sri Prakash -
4. Mr. N. Gowtham Kumar -

Names & signatures of Internal members:

1. Dr. P. Sobha Rani -
2. Mr. T. Sesha Sai Babu -
3. Dr. M.K.Sastry -
4. Mr.J.SivavaraPrasad -
5. Mr. P. Deepak Reddy -

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Annexure-Minutes of BOS Meeting held on 04-03-2017

After thorough discussions and deliberations, following resolutions are arrived in the proposed **R17 Course Structure**:

I. Title changes for courses:

1. (EST5)-"Basic Electronic Devices and Circuits" course title is changed as "Electronic Circuits and Devices".
2. (PCL1)-"Basic Electronic Devices and Circuits Lab" course title is changed as "Electronic Circuits and Devices Lab".
3. (PCT3 & PCT6)-"Electrical Circuits -I & II" courses titles are changed as "Network Theory -I & II" respectively.
4. (PCL2)-"Electrical Circuits Lab" course title is changed as "Electrical Networks Lab".
5. (PCT7)-"Analog Electronics" course title is changed as "Electronic Circuit Analysis".
6. (PCT9)-"Linear Digital IC Applications" course title is changed as "Linear and Digital Integrated Circuits"
7. (PCL5)-"Analog Electronics & IC Applications Lab" course title is changed as "Electronic Circuits and ICs Lab".
8. (EST6)-"Elements of Signal Processing" course title is changed as "Analog and Digital Signal Processing".

II. Other changes in course Structure:

1. "Digital Logic Circuit Design" course (in Sem –II) is swapped with "Electronic Devices and Circuits" Course (in Sem-III).
2. "Digital Logic Circuits Lab" Course (in Sem-II) is swapped with "Electronic Devices and Circuits Lab" course (in Sem-III).
3. It is suggested to remove 2 contact hours shown in the course structure for (IPL3)-"Internship" because it is only the evaluation done in Sem-VII.

III. List of Program Electives (PEs):

PE-I	PE-II
1. Optimization Techniques in Engineering	1. Digital Controllers
2. Renewable Energy Technologies	2. Embedded Systems
3. Electrical Engineering Materials	3. Data Communications and Networking

PE-III	PE-IV
1. Intelligent Control Systems	1. Energy Conservation and Audit
2. Digital Control Systems	2. PLC and Automation
3. Advanced Control Systems	3. Energy Storage Systems

PE-V	PE-VI
1. Special Electrical Machines	1. Power Quality
2. Modeling and Analysis of Electrical Machines	2. HVDC & FACTS
3. Advanced Power Converters	3. Smart and Micro Grid

IV. **List of Open Electives (OEs):**

1. Utilization of Electrical Energy
2. Energy Audit
3. Renewable Energy Sources

V. List of Add - On Courses (AoCs):

AOC-1	AOC-2	AOC-3
1. Sustainable Energy Systems	1. EHVAC Systems	1. Optimization Techniques in Power Systems.
2. Computer Aided Electrical Machine Design	2. Electrical Reliability Engineering	2. High Voltage Engineering
3. Electrical Safety	3. Switched Mode Power Conversion	3. Electric Vehicle Engineering

VI. Changes in Course Content:

1. *ESL4*-Electrical Workshop

Exp – 5: Study of different types of house wirings and conductor gauge

Exp- 7 : Study of different types of lamps (Fluorescent , LED, Sodium – Vapour, Neon, incandescent etc.)

Exp – 11: Study of electrical & electronic measuring devices (Voltmeter, Ammeter, wattmeter, multi meter, Tong tester, Lux meter, anemometer, pressure gauge, clamp meter and Meggar)

Exp – 13: Significance of Earthing and its test.

2. PCT3-Network Theory – I

Units are re-organized as follows:

Unit - I : Introduction to Electrical Circuits

Unit - II : Magnetic Circuits

Unit - III : Network Topology

Unit - IV : Transients

Unit - V: Signal phase AC circuits

Unit - VI : Resonance

3. **PCT6-Network Theory – II**

Units are re-organized as follows:

Unit - I : Network Theorems

Unit - II: Three-Phase balanced circuits

Unit - III:Three-Phase unbalanced circuits

Unit -IV:Two-Port Networks

Unit -V:Non-Sinusoidal AC Circuit Analysis

Unit -VI: Network Synthesis

4. **PCT 5 – Power Generation and Utilization**

Units – I, II, V & VI are retained without changes.

Unit – III is split into two units (III&IV) as follows:

- UNIT-III: Economical aspects of Power generation
- Unit – IV: Tariff methods &

Unit – IV original content is added in "PCT11 – Electrical Power Transmission" course of V-semester.

5. **"PCT11- Electrical Power Transmission" course content is to be reframed with the following changes:**

i) "Per unit quantities" content to be added here which is removed from "Power Generation and Utilization" course.

ii) "Over voltage protection" content is to be removed from EPT course and added in "PCT-16-Power system Protection course".

6. **"PCT16 – Power System Protection" course content to be reframed with the following changes:**

"Over voltage protection" content is to be added here which is removed from "PCT11-EPT" course.

7. **EST6-Analog and Digital Signal Processing**

"Analog and Digital Signal Processing**"** course content is to be reframed ~~and~~ analog signal processing content is to be incorporated in unit I.

VII. Suggested changes in Continuous Internal Evaluation (CIE):

- Assignments component of CIE which is 'six' assignments per course (as proposed in R17 rules and regulations) is suggested to limit to a maximum of 'three' assignments per course.

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M. Umavani

(Dr.M.UmaVani)

HoD/EEE